Invasive Computing

DRR summary

IrRsee, 8th to 10th October 2012
1st Day: Tutorial & granularity day

• Tutorial: Monitoring and Measuring on Invasive Hardware

• Tutorial: Multi-Tile OctoPOS guest layer

• Core/Tile Granularity
2nd and 3rd Day

• Visiting the **brewery**

• *Dynamically invasive* chosen fallback solution to GeoCaching (due to bad weather): **Abbey** tour

• **Configuration** of FPGA/ChipIT
  More information available during next presentations

• Plenty of small group discussions (cont'd on next slide)
Small Group discussions

- IO over ethernet (API)
- Simulator and monitor data, API in simulator
- Performance graphs on hybrid memory systems
- iNoC Guarantees
- Demand for more FPGA boards
- Malleable interface
- Monitoring TCPAs
- Invasion granularity
- Leon Configuration and Grlib versions
- Power monitoring

- Agent system and TCPA
- Agent system and i-Core
- Implementing X10's at
- Bootup of multi-tile and debugging on CHIPit and eval boards
- Memory sizes
- TCPA applications
- Agent optimization level
- Simulator architecture

Detailed information available in the WIKI (as requested)!!!
https://invasic.informatik.uni-erlangen.de/intern/dokuwiki/mitgliederversammlungen/drr2012-2/index
Thank you for your dynamical attention

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