Bridging the gap between embedded systems and automation systems

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Embedded Systems



^aYoong et al. Model driven design using IEC61499, Springer 2015.

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Automation Systems



^aYoong et al. Model driven design using IEC61499, Springer 2015.

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Distinction is blurring

- Real-time control using PLCs alone is not feasible any longer.
- PLCs are combined with FPGAs to bridge the gaps in timing. ^a:
- Using an FPGA allowed for encoding position signals to be handled directly from the sensors. No intermediate processing or amplification device was required, thereby reducing noise and increasing processing speed. In a process cycle faster than 1 ms, the valve position is measured and speed is calculated as both are compared to the set point. Movement is corrected using a PID algorithm. To keep the hydraulic circuit balanced, pressure values in the front and back of the cylinder are simultaneously controlled to avoid instantaneous peaks.
- Question: How to develop a systematic approach that bridges the gap without using ad-hoc solutions?

^aGreenfiled D (2013) How embedded systems are changing automation, Automation World.

- A family of languages developed in France in the early 80s, with identical view of concurrency. ^a.
- Inspired by synchronous circuits: all components trigger relative to a global clock.
- The reactive system operates *infinitely fast* relative to its environment. This is known as the synchrony hypothesis.
- Interleaving disappears in this semantics due to strict notions of causality.

 $^{^{}a}$ A. Benveniste et al., The synchronous languages twelve years later, proceedings of IEEE, 91(1), 2003

IEC61499 – a cruise controller



(University of Erlangen-Nuremberg)

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Synchronous approach for IEC61499



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4.3.1.6 Sequential statement

Rule 4.11 expresses the fact that the sequence does not finish, if its left branch, t, does not.

$$\frac{t, D \xrightarrow{O,\perp}_{l^p} t', D'}{t; u, D \xrightarrow{O,\perp}_{l^p} t'; u, D'}$$
(4.11)

If the left branch pauses, so does the sequence.

$$\frac{t, D \xrightarrow{\emptyset, 1}_{I^p} t', D}{t; u, D \xrightarrow{\emptyset, 1}_{I^p} t'; u, D}$$
(4.12)

Moreover, if the left branch raises an exception (by exiting a trap), its right branch will never get executed.

$$\frac{\mathbf{t}, D \frac{\mathbf{0}, k}{I^p} \mathbf{t}', D \qquad k \ge 2}{\mathbf{t}; u, D \frac{\mathbf{0}, k}{I^p} \mathbf{t}', D}$$
(4.13)

Otherwise, control will be immediately transferred to the right branch, u, when t finishes.

$$\frac{t, D \xrightarrow{\emptyset, 0}}{l^{p}} t', D \qquad (4.14)$$

$$t; u, D \xrightarrow{\emptyset, \bot}{l^{p}} u, D \qquad (4.14)$$

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Delayed composition for causality



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Semantics: parallel

$$\frac{\mathfrak{t}, D \frac{\mathcal{O}, \bot}{l^{p}} \mathfrak{t}', D'}{\mathfrak{t} \| \mathfrak{u}, D \frac{\mathcal{O}, \bot}{l^{p}} \mathfrak{t}' \| \mathfrak{u}, D'}$$

$$(4.15)$$

$$\frac{\mathfrak{u}, D \frac{\mathcal{O}, \bot}{l^{p}} \mathfrak{u}', D'}{\mathfrak{t} \| \mathfrak{u}, D \frac{\mathcal{O}, \bot}{l^{p}} \mathfrak{t} \| \mathfrak{u}', D'}$$

$$(4.16)$$

Rule 4.17 uses the completion code synchronizer to specify the synchronized behaviour of the parallel statement. When both t and u perform finished transitions, the parallel statement synchronizes their execution using their completion codes.

$$\frac{t, D \xrightarrow{\emptyset,k}}{I^{p}} t', D \quad k \ge 0 \qquad u, D \xrightarrow{\emptyset,l} u', D \quad l \ge 0$$
$$t \parallel u, D \xrightarrow{\emptyset, syn(k,l)} t' \parallel u', D \qquad (4.17)$$

As already mentioned in the description for data assignment, write-write concurrency on variables is disallowed, while read-write concurrency is semantically forbidden by rule 4.8. This means that t and u will never operate on the same variables in the same instant.

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^aYoong et al. A synchronous approach for IEC 61499 function block implementation, IEEE Transactions on Computers, 58(12), 2009.

- Determinism: Given any state and any valid input combination, at most one transition is enabled.
- Reactivity: Given any state and any valid input combination, at least one transition is enabled.
- Theorem: Synchronous function blocks are deterministic and reactive.
- The unification of embedded systems and automation systems requires the consideration of cyber-physical systems i.e. not just the controller but also the plant.

^aYoong et al. A synchronous approach for IEC 61499 function block implementation, IEEE Transactions on Computers, 58(12), 2009.

Cyber-physical systems (CPS)^a use distributed embedded controllers to control physical processes. Examples may be found in several domains: automotive, robotics, medical devices, and smart grids.

^aR. Alur, Principles of Cyber-Physical Systems. MIT Press, 2015.



¹Figure reproduced from http://icc.mtu.edu/cps/

- Hybrid automata (HA) is a major enabler for the formalization of CPS.
- A combination of ODEs to model the continuous dynamics and FSMs to model the discrete mode changes that are induced by the controller.



Model: Car Discrete: Changing gears Continuous: Throttle control



Model: Cell biology Discrete: External stimulus Continuous: Flow of ions

A water tank temperature controller



^aJ-F Raskin, "An introduction to hybrid automata", Handbook of Networked and Embedded Control Systems Control Engineering 2005, pp 491-517.

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A water tank temperature controller



• Temperature of water inside a tank may be modelled as $x(t) = le^{-Kt} + h(1 - e^{-Kt})$ where:

- I is the initial temperature.
- K is a constant that depends on the tank conductivity.
- *h* is a constant that depends on the power of the gas burner.

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- Four locations t1,..t4 that represent the discrete modes.
- Each location has some flow predicates that specify the rate of change of the continuous variables.

^a J-F Raskin, "An introduction to hybrid automata", Handbook of Networked and Embedded Control Systems Control Engineering 2005, pp 491-517.



- Invariants are associated with locations e.g. 20 ≤ x ≤ 100 is an invariant associated with t1. Execution remains in a location until the invariant holds.
- Some locations may have initialization conditions that provide the initial values of the variables.
- A transition is enabled when the input is present and the jump condition associated with the transition holds. When a given transition is taken the final value of the variables are updated.



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Definition

A hybrid automata H = < Loc, Edge, Σ , Inv, Flow, Jump > where

- $Loc = \{l_1, ..., l_n\}$ representing *n* control modes or locations.
- Σ is the input alphabet comprising of event names.
- $Edge \subseteq Loc \times \Sigma \times Loc$ are the set of edges between locations.
- Three sets for the set of continuous variables, their rate of change and their updated values represented as follows: X = {x₁,..,x_m} X = {x₁,..,x_m} X' = {x₁,..,x_m}.
- *Init(1)*: Is a predicate whose free variables are from X. It specifies the possible valuations of these when the HA starts in *I*.
- Inv(1): Is a predicate whose free variables are from X and it constrains these when the HA resides in I.
- Flow(I): Is a predicate whose free variables are from X ∪ X and it specifies the rate of change of these variables when the HA resides in I.
- Jump(e): Is a function that assigns to the edge e a predicate whose free variables are from X ∪ X'. This predicate specifies when the mode switch using e is possible. It also specifies the updated values of the variables when this mode switch happens.

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Definition

The semantics of a HA H= $< Loc, Edge, \Sigma, Inv, Flow, Jump >$ is provided using a timed transition system TTA = $< Q, Q_0, \Sigma, \rightarrow >$

- Q is for the form (1, v) where I is a location and v ∈ [X → R] such that v satisfies Inv(1). Q is called the state space of H.
- $Q_0 \subseteq Q$ of the form (I, v) such that v satisfies Init(I).
- ullet
 ightarrow is the set of transitions consisting of either:
 - Discrete transitions: For each edge $e = (I, \sigma, I')$, we have $(I, v) \xrightarrow{\sigma} (I', v')$ if $(I, v) \in Q$, $(I', v') \in Q$ and (v, v') satisfy Jump(e). These take zero time.
 - Continuous transitions: When control remains in a location and time progresses. Here the continuous variables evolve according to the ODEs as long as the invariant holds.

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- This validation is usually done open-loop.
- System under validation is stimulated using an input trace to observe its response.
- Limitations: Coverage criteria dependent, exhaustive simulation infeasible.



Simulation for validating a pacemaker

- Operating a **controller** under test in closed-loop with the actual physical process (the **plant or the environment**) [5].
- The design of the controller follows the principles of real-time systems.
- The controller is digital in nature, while the plant usually exhibits continuous dynamics and is uncontrollable.



Emulation for validating a pacemaker (Actual heart + pacemaker model)
- The plant and the controller may need to be designed in parallel i.e. a rehabilitation robot.
- Model-in-the-loop simulation using Simulink and Stateflow: semantic issues [7, 1] and issues with model fidelity.
- Ptolemy [6] and Zélus [1] are tools with formal semantics. However, these are suitable for the modelling of closed systems using HA models. Also, like SL/SF they interact dynamically with ODE solvers. This is not good for emulation.
- Potemy has incorporated a QSS-based solver [2] to overcome the above. This, however, is unsuitable for open systems.

There is no approach for black-box validation of controllers (say Pacemakers) using real-time plant models. This requires:

- Open models of the plant using a network of hybrid input output automata (HIOA [4]).
- Formal semantics of HIOA models and their compositions.
- Automatic techniques for modular code generation.
- Static timing analysis of the plant for plant-controller timing compatibility i.e. correct timing verification to ensure that the sampling time of the plant and controller match [3]

We propose the new technique of remulation for this.

What is remulation?

Remulation stands for reverse emulation using an executable model of the plant, we term a plant-on-a-chip (PoC). During remulation, the plant-controller relationship is reversed.

- We have to synthesize a suitable model of the r-controller (the traditional plant).
- The r-plant (the usual controller) acts as an environment for the r-controller. The r-plant is black-box in nature.



Remulation for validating a pacemaker (Heart model (real-time) + pacemaker actual/model)

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Methodology overview



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Compilation overview

- The first step of the compilation procedure
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Compilation overview



Figure: Overview of the proposed modular code generation approach

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Well formed HIOA (WHIOA)

- Discretisation: In HA semantics, when control resides in a location, there are infinite valuations of variables in any given interval of time. This makes code generation difficult. To facilitate code generation, we make evaluations only at discrete intervals. These intervals correspond to the *ticks* of a synchronous program that will be used for code generation.
- Symbolic solution: The ODEs which define flow constraints in any location of the form $\dot{x} = f(x)$ must be of *closed form* nature. This property ensures that such ODEs are symbolically solvable so that the witness functions needed for the generated code are symbolically computable.
- Monotonicity: All witness functions must be monotonic. This property is needed so that the generated code can compute correct valuation of invariants and jump conditions.

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Recap: the heating system



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Step-1: translating ODEs to witness functions

Symbolic approach using the synchronous abstraction

$$\dot{x} = K(h-x), \ K = 150, \ h = 0.075$$

 $x[k] = C_1 \times e^{-0.075 \times \delta \times k}$

Explanation

- Equation (1), Ordinary Differential Equation (ODE) captures the evolution of the continuous variable x that represents the temperature in the tank.
- The witness function, for the ODE, is the symbolic (closed form solution) to the ODE, if one exists.
- Equation (2) evolves x iteratively while the invariant condition (10 ≤ x ≤ 100) on the location (t₂) holds.
- This iterative evolution of the continuous variables at discrete points in time is akin to transitions on a logical *tick* of a synchronous program.
- We term the Hybrid Input Output Automata (HIOA) obtained after replacing each ODE with its equivalent witness function Synchronus Hybrid Input Output Automata (SHIOA).

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Compilation step 2: Compiling HIOA to SHIOA



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$${}^{a}F1 = C1 imes e^{-0.075 imes t} + 150.0$$

and $F2 = C1 imes e^{-0.075 imes t}$

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Compilation step 3: SWIOA / back-end code generation



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Need for saturation



Figure: The need for saturation depends on the location invariant, the guard in HA, and the step size. Out of the three cases, only Case 2 requires saturation, see Figure 2(d).

Lemma

It is always possible to uniquely determine the saturation value for any continuous variable at time instant k when the state (location) switch from I to I' is to be taken in a SHIOA.

Proof.

The proof of this lemma follows from the following observations.

- Observation 1: All witness functions x(t) for any x ∈ X are monotonic in every location (additional requirement).
- Observation 2: All witness functions x(t) for any x ∈ X are continuous as they are differentiable in any interval.
- Observation 3: Given the above two observations, the saturation value for any variable x always exists in the time interval $[(k-1) \times \delta, k \times \delta]$ when the location switch happens at instant $k \times \delta$.

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UoA Model - Cell



HA based on Ventricular AP.

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Simulink vs Piha - Execution time



Simulink[®] Piha (O0) Piha (O2)

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Scalability Relative to Simulink



Methodology for PoC design for automation



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Conclusions

- We discuss the need for a unified design approach for embedded / automation systems.
- We adopt the well known synchronous approach for designing the controller (using the IEC61499 standard) and the plant (also using the same standard).
- We propose a new technique called reverse emulation (remulation) to design a plant-on-a-chip (PoC).
- A PoC is a model of the plant that offers real-time response similar to the actual plant.
- A key idea is to avoids dynamic interaction with numerical solvers to enable real-time implementations.
- The approach is based on a new class of hybrid automata, we call synchronous hybrid automata.
- We have compared our approach with Simulink and the results are favourable.

• Future work: comparison with Ptolemy and the QSS-based approach, (University of Erlangen-Nuremberg) Invasive Computing Seminar

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